

Logical Design For Digital Systems

INTRODUCTION

LOGICAL DESIGN FOR DIGITAL SYSTEMS is one of a series of advanced programs developed through field research by RCA Institutes' School of Custom Educational Programs. This five-day seminar presents applications of the latest engineering techniques for the logical design of digital systems.

SCOPE

Today's new digital techniques far outweigh classical logic and Boolean Algebra as logic design tools; these new methods are explored and developed in this seminar. Powerful, rapid design procedures based on designation numbers, logic matrices, and graphical synthesis are presented. Emphasis will be placed on the "Building Block" concept, an approach which permits the application of these techniques to such diverse areas as electronics, magnetics, fluid logic and mechanics.

PREREQUISITES

This presentation is directed to engineers and technical personnel concerned with the theory, design and operation of complex switching or digital systems. The minimum requirements are a baccalaureate degree in mathematics, engineering, or physics, or the equivalent. Experience in switching circuits or digital systems may be substituted.

STAFF

This seminar will be presented by members of the RCA Institutes' School of Custom Educational Programs, under the direction of Bradford Daggett, Director
Alfred B. Peticolas, Dean, Program Coordination
Matthew V. Mahoney, Dean, Technical Programs
Abraham Schwartz, Member of Technical Staff
Mario C. Laguzzi, Member of Technical Staff
and

The Board of Technical Advisers, representing various technical, research and educational activities of the Radio Corporation of America and its subsidiaries.

SEMINAR OUTLINE

FIRST DAY

NUMERICAL APPROACH TO LOGIC

Number systems; binary coding; set theory and logic equations; the designation number concept.

SECOND DAY

COMBINATIONAL DESIGN

Application of designation numbers; the input and output matrices; design with matrices; and the numbered logic map; combinational design procedures.

THIRD DAY

DESIGN FOR RELIABILITY AND ECONOMY

Logical dependence; the constrained matrix; standard computer packages; NAND-NOR implementation.

FOURTH DAY

TIME SEQUENTIAL NETWORKS

Feedback and recursive design; RS, type D, type T, and JK flip-flop; ripple-through and parallel counters; parallel and serial arithmetic.

FIFTH DAY

DIGITAL APPLICATIONS

Utilizing counters and shift registers; multiple function design; synthesis of wavetrains; encoding and decoding matrices.

SEMINAR INFORMATION

Seminars will be limited in size. Registrations will be accepted in the order received. Substitution of applicants may be made at any time. Cancellations will be honored and fee refunded provided notification is received no later than two weeks prior to the seminar.

Hotel Accommodations—RCA Institutes reserves a block of rooms in each seminar hotel. Please make reservations directly with the hotel, with reference to RCA Institutes' seminar.

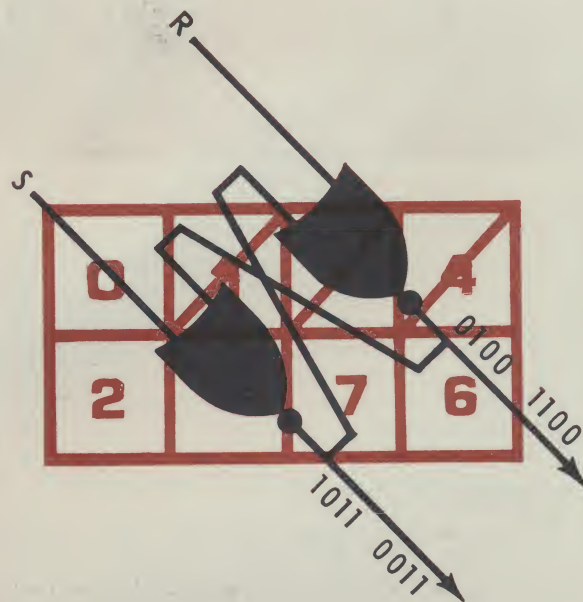
Registration fee includes attendance, luncheon, coffee break, and a complete package of reference material. Telephone answering service is provided during seminar hours.



RCA INSTITUTES, Inc.

School of Custom Educational Programs

350 West 4th Street
New York, N. Y. 10014
Area Code 212 YU-9-2093



RCA INSTITUTES
Announces
A Five-Day Seminar —

**LOGICAL
DESIGN
FOR
DIGITAL
SYSTEMS**

1966 Version



RCA INSTITUTES, Inc.
School of Custom Educational Programs
350 West 4th Street • New York, N. Y. 10014

BRADFORD DAGGETT
DIRECTOR

AREA CODE 212
YUKON 9-2093

RCA Institutes Announces an Important Seminar --
LOGICAL DESIGN FOR DIGITAL SYSTEMS --
To Be Presented in New York City

This seminar, which has earned wide acclaim in all previous presentations, will develop many of the latest methods of logic design. Emphasis will be placed on the concepts of functional circuit synthesis utilizing standard "Logic Building Blocks."

The design principles developed will stress applied logic and will be utilized to design and construct logic circuitry on demonstration equipment. The effectiveness and versatility of these techniques will be thoroughly explored and presented in an easy-to-understand format with extensive use of exercises.

The place: Hotel New Yorker
 34th Street at Eighth Avenue
 New York City, New York

The dates: February 28 - March 4, 1966;
 9:00 a.m. to 4:30 p.m. daily

This 5-day seminar is intended for Design and Development Engineers, Educators, and others who have a desire to obtain a clear and coherent understanding of the principles and techniques of logic design.

We urge you to register promptly, since attendance will be limited. A folder covering the seminar as well as a registration form is enclosed. Hotel Reservations should be made directly with the hotel, phone (212) LO 3-1000.

Please call or write us, should you have any questions.

Sincerely,

Bradford Daggett

BD/LDD-3

Postage
Will be Paid
by
Addressee

No
Postage Stamp
Necessary
If Mailed in the
United States

BUSINESS REPLY MAIL

FIRST CLASS PERMIT NO. 10662, NEW YORK, N. Y.

RCA INSTITUTES, Inc.
350 West 4th Street
New York, N.Y. 10014

CUSTOM EDUCATIONAL PROGRAMS

REQUEST FOR SEMINAR REGISTRATION(S)

To: RCA INSTITUTES, Inc.
School of Custom Educational Programs
350 West 4th Street, New York, N. Y. 10014

REGISTRATION RATE PER 5-DAY SEMINAR — \$350.00

LOGICAL DESIGN FOR DIGITAL SYSTEMS ~

NEW YORK CITY
FEB. 28 — MARCH 4, 1966

☐ Purchase Order Enclosed

☐ Check Enclosed

☐ Bill Company

☐ Bill Me

Please register the following persons for the seminar, indicated above.

NAME _____

TITLE _____

NAME _____

TITLE _____

NAME _____

TITLE _____

AUTHORIZED BY _____

signature

title

FIRM'S NAME AND ADDRESS _____

CE 204-2

☐ Additional Hotel Reservation Information Requested

Please make checks payable to RCA Institutes, Inc.

FUTURE SEMINAR ANNOUNCEMENTS

Gentlemen:

Please place the following names
on your mailing list for future seminar announcements:

name _____ title _____

name _____ title _____

name _____ title _____

firm's name _____

address _____ zip code _____

CE-102

A partial list of the companies that have sent personnel to attend our computer seminars

AMERICAN MACHINE AND FOUNDRY CO.
BELL TELEPHONE LABORATORIES
BENDIX CORP.
BURROUGHS CORP.
COLGATE-PALMOLIVE CO.
COLUMBIA BROADCASTING SYSTEM
CONTROL DATA CORP.
CORNING GLASS WORKS
FAIRCHILD CAMERA AND INST. CORP.
GENERAL DYNAMICS ELECTRONICS
GENERAL MOTORS CORP.
GRUMMAN AIRCRAFT ENGINEERING CORP.
HONEYWELL INC.
INTERNATIONAL BUSINESS MACHINE CORP.
MARTIN CO.

PITNEY-BOWES, INC.
PROCTER & GAMBLE CO.
RADIO CORPORATION OF AMERICA
RAYTHEON CO.
SPERRY RAND CORP.
SPRAGUE ELECTRIC CO.
STROMBERG-CARLSON CO.
SYLVANIA
THE BUDD CO.
THE MITRE CORP.
UNITED AIR LINES
UNIVERSAL CIRCUIT CONTROLS
WESTERN ELECTRIC CO.
WESTERN UNION TELEGRAPH CO.
WESTINGHOUSE ELECTRIC CORP.
XEROX CORP.